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Robin E. Barnum (Depositor's name)

(Signature)

January 18, 2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/008,696	11/08/2001	Jay B. Reimer	TI-30105	7469

TITLE OF INVENTION: MULTICORE DSP DEVICE HAVING COUPLED SUBSYSTEM MEMORY BUSES FOR GLOBAL DMA ACCESS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	04/06/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
DANG, KHANH	2111	710-305000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Robert D. Marshall, Jr.

2 W. James Brady, III

3 Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Texas Instruments Incorporated

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Dallas, TX USA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Robert D. Marshall, Jr.

Date January 18, 2005

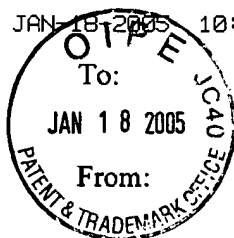
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Robert D. Marshall, Jr.
Texas Instruments Incorporated
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Phone: 972-917-5290**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

TI-30105

Jay B. Reimer, et al.

Art Unit: 2111

Serial No.: 10/008,696


Examiner: Khanh Dang

Filed: October 30, 2001

Conf. No.: 7469

For: Multicore DSP Device Having Coupled Subsystem Memory Buses for Global DMA
Access**CERTIFICATION OF FACSIMILE TRANSMISSION**

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Robin E. BarnumJanuary 18, 2005
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NAME OF INVENTOR(S): Jay B. Reimer, et al.	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/008,696 Filing Date: October 30, 2001	
TITLE OF INVENTION: Multicore DSP Device Having Coupled Subsystem Memory Buses for Global DMA Access	
TI FILE NO.: TI-30105	DEPOSIT ACCT. NO.: 20-0668
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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265